

FIG. 1

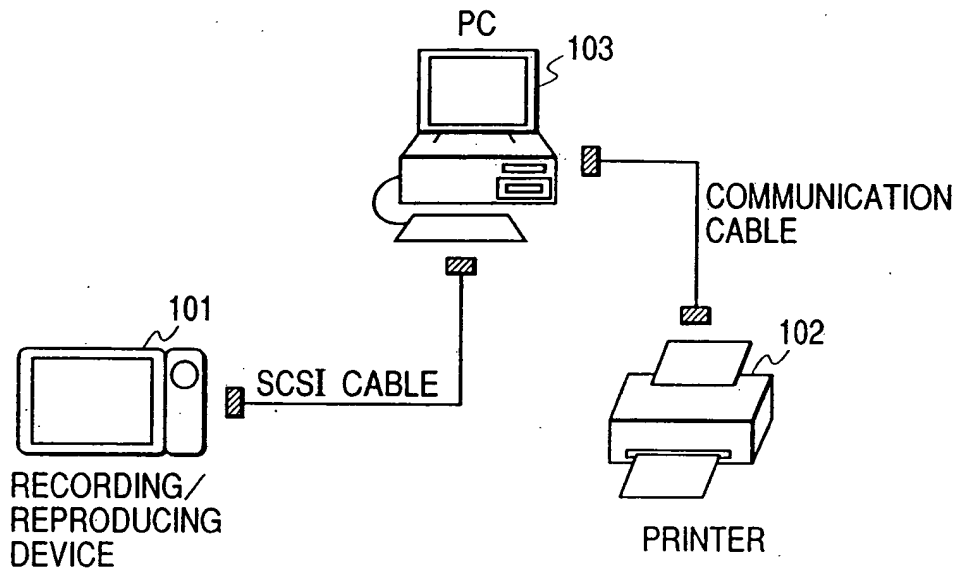


FIG. 2

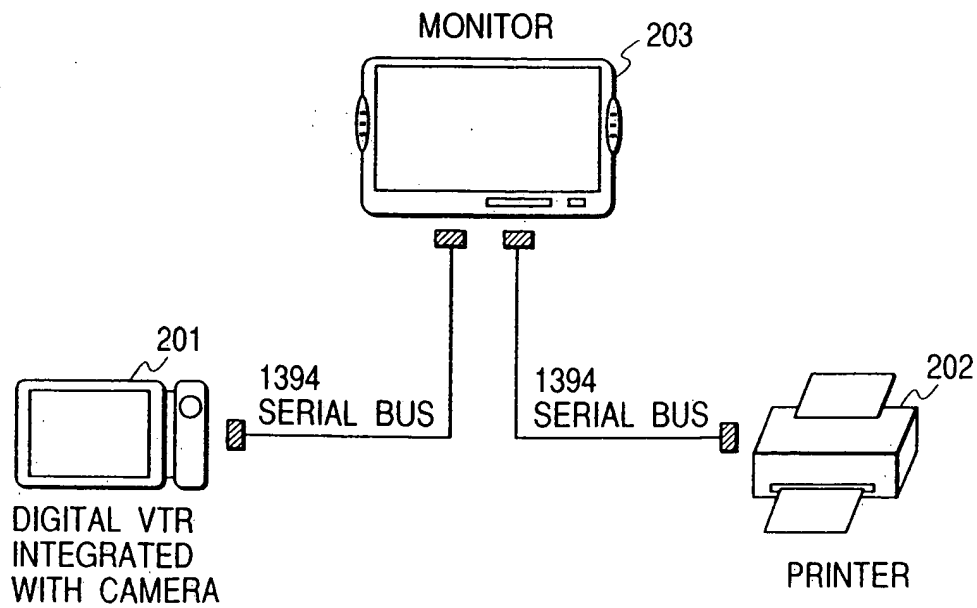


FIG. 3

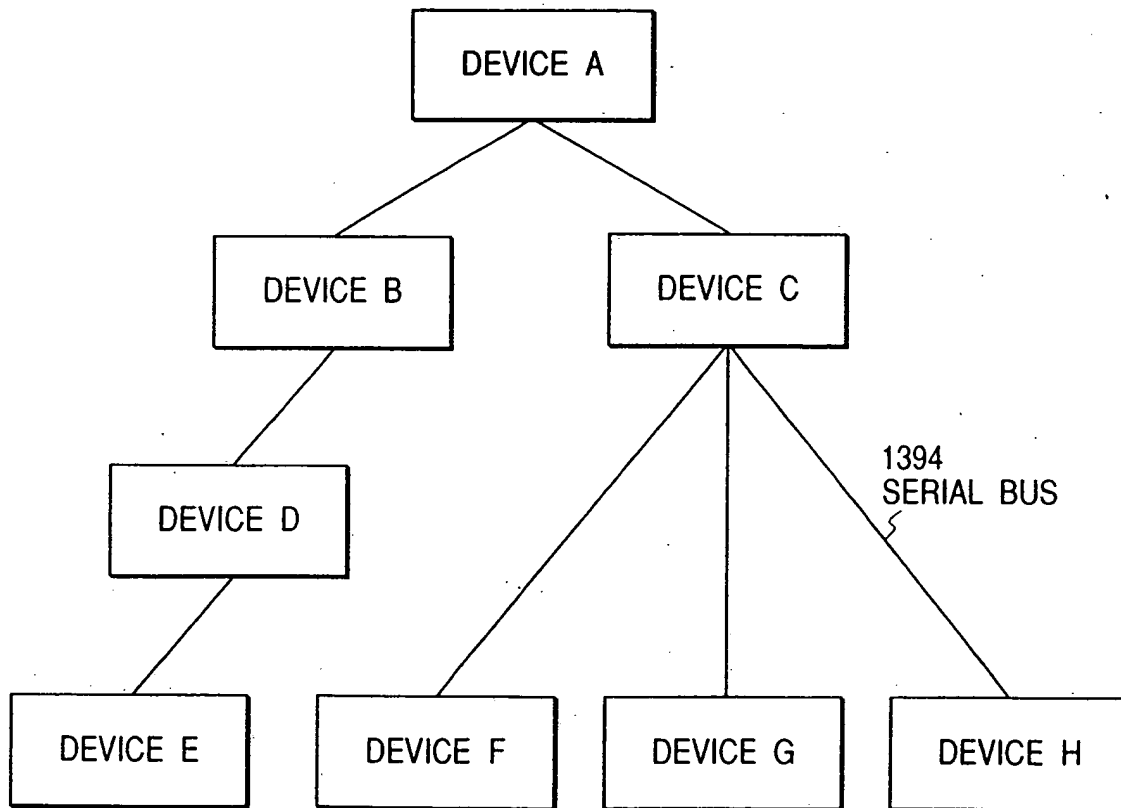


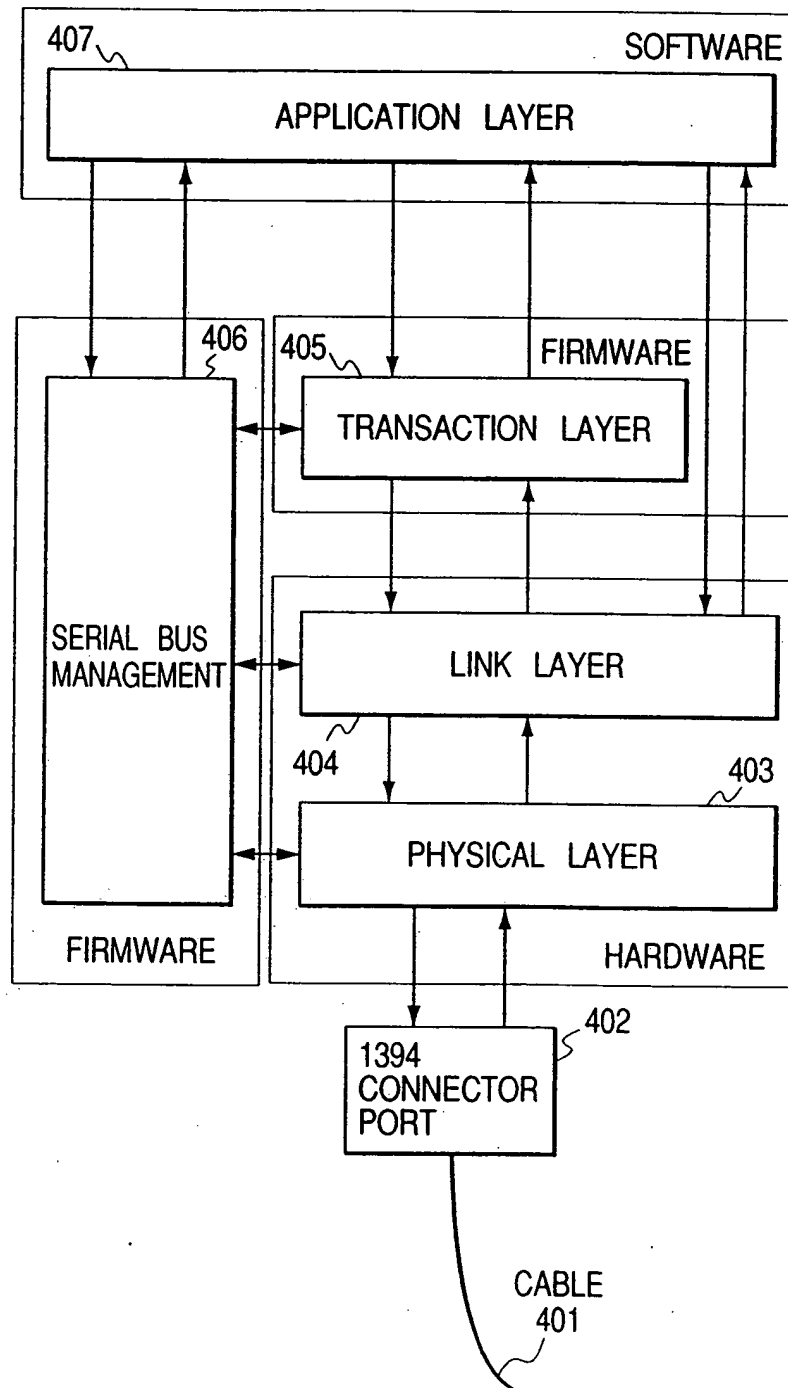
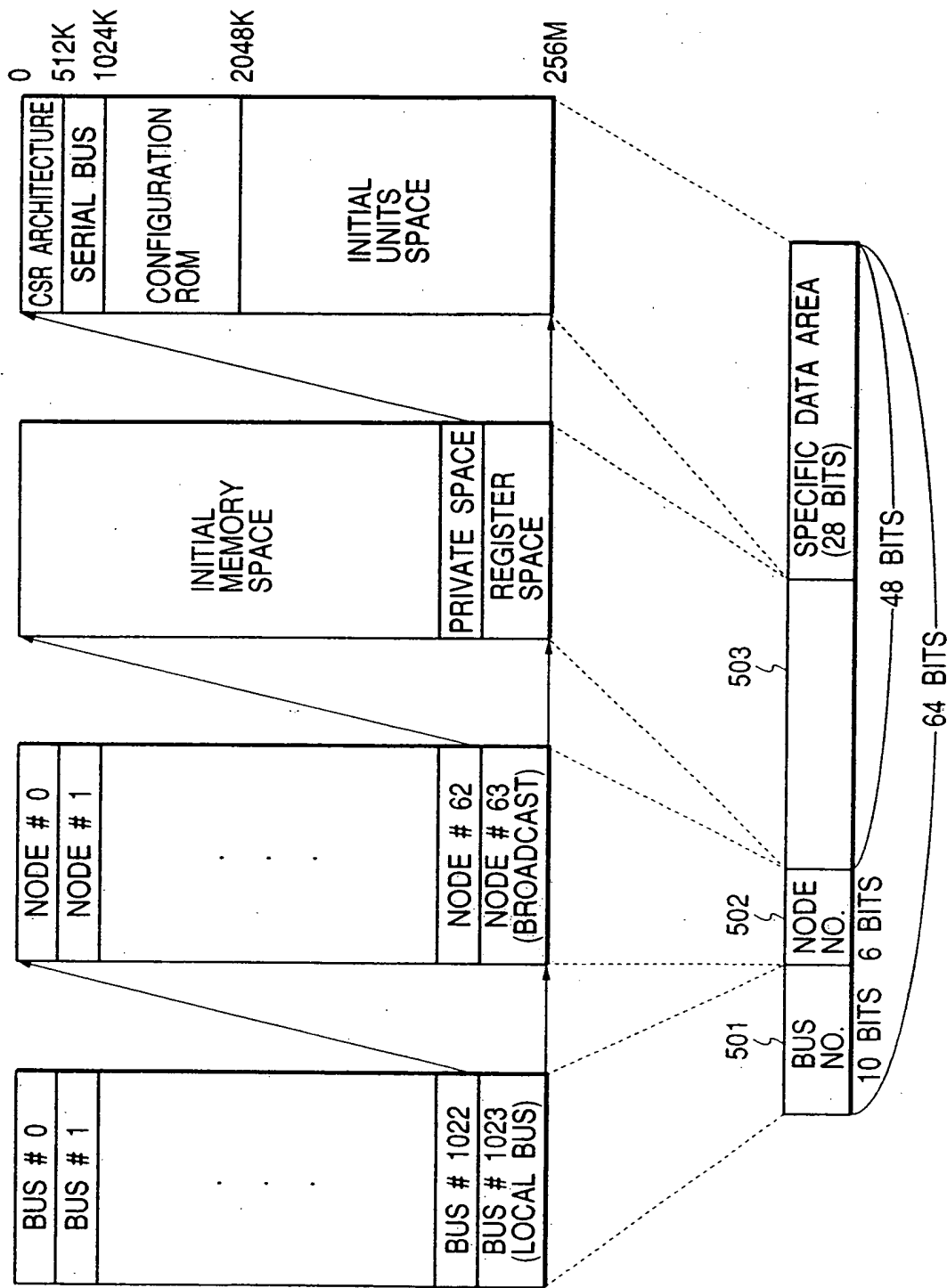
FIG. 4

FIG. 5



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FIG. 6

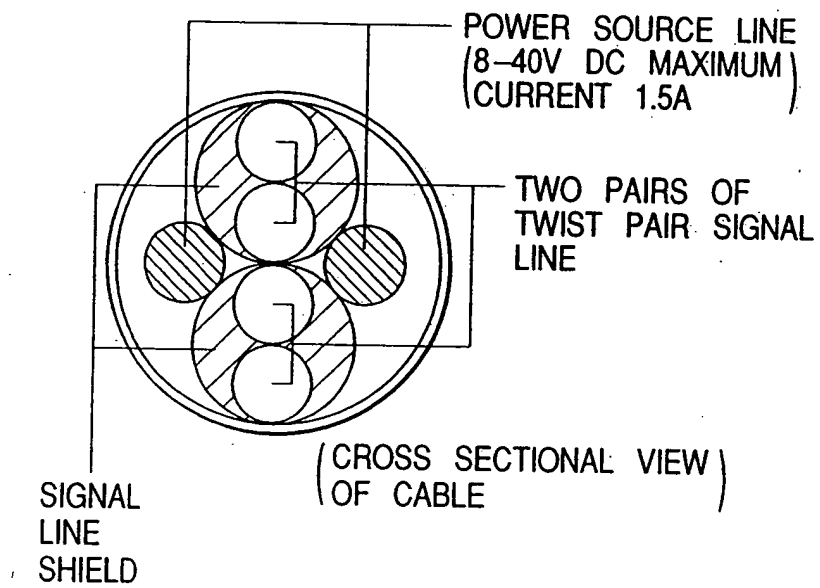


FIG. 7

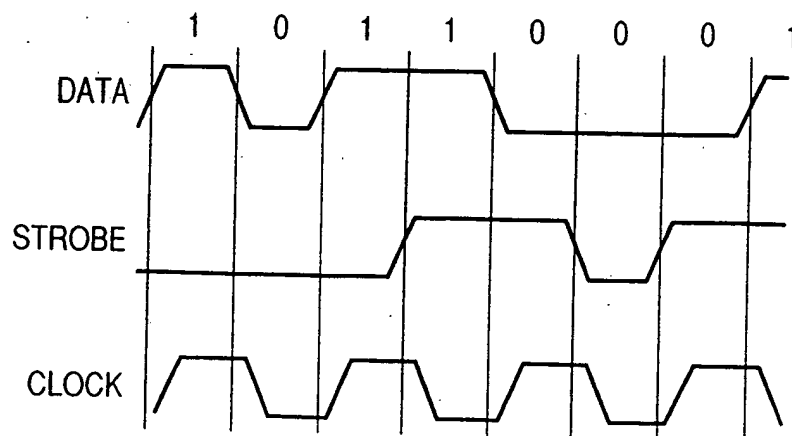
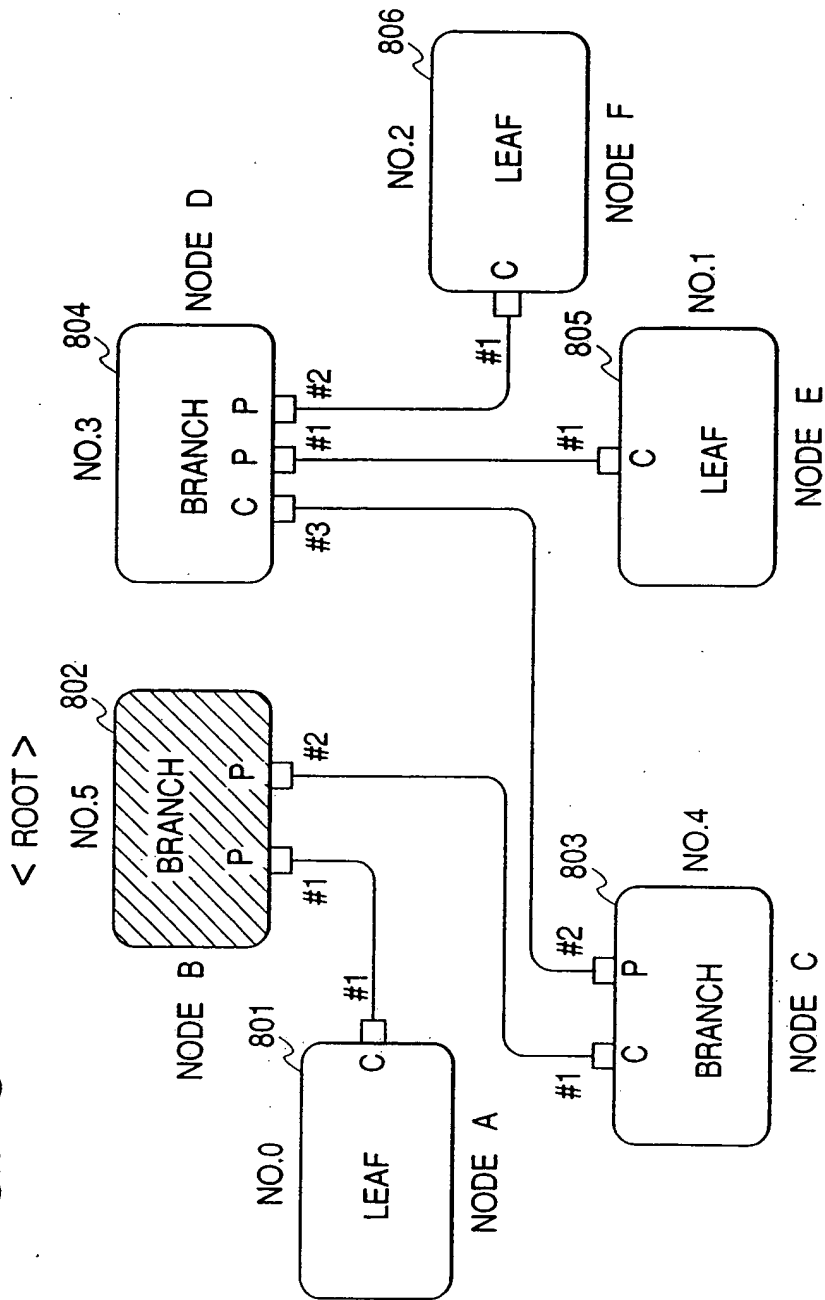


FIG. 8



BRANCH : NODE CONNECTED TO AT LEAST TWO NODES
 LEAF : NODE CONNECTED TO ONLY ONE NODE

FIG. 9

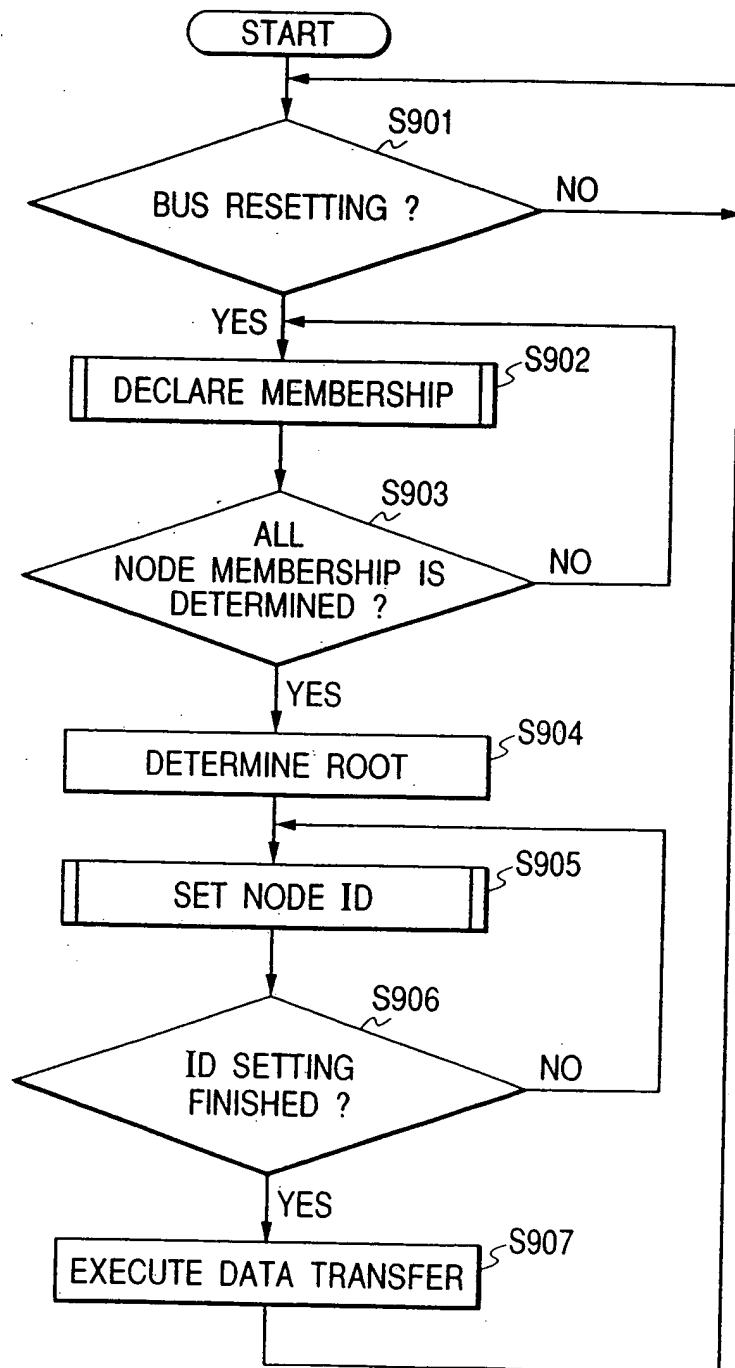


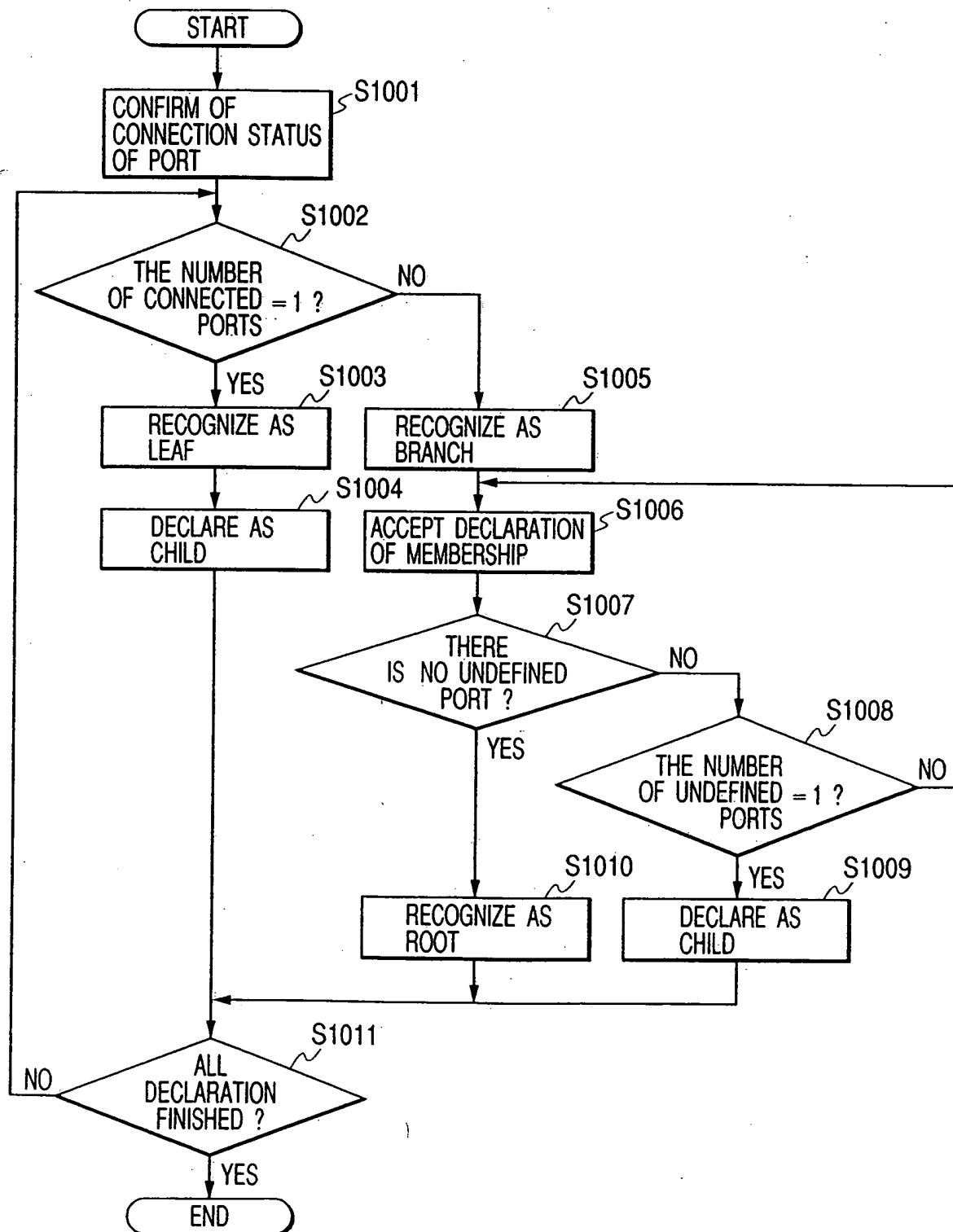
FIG. 10

FIG. 11

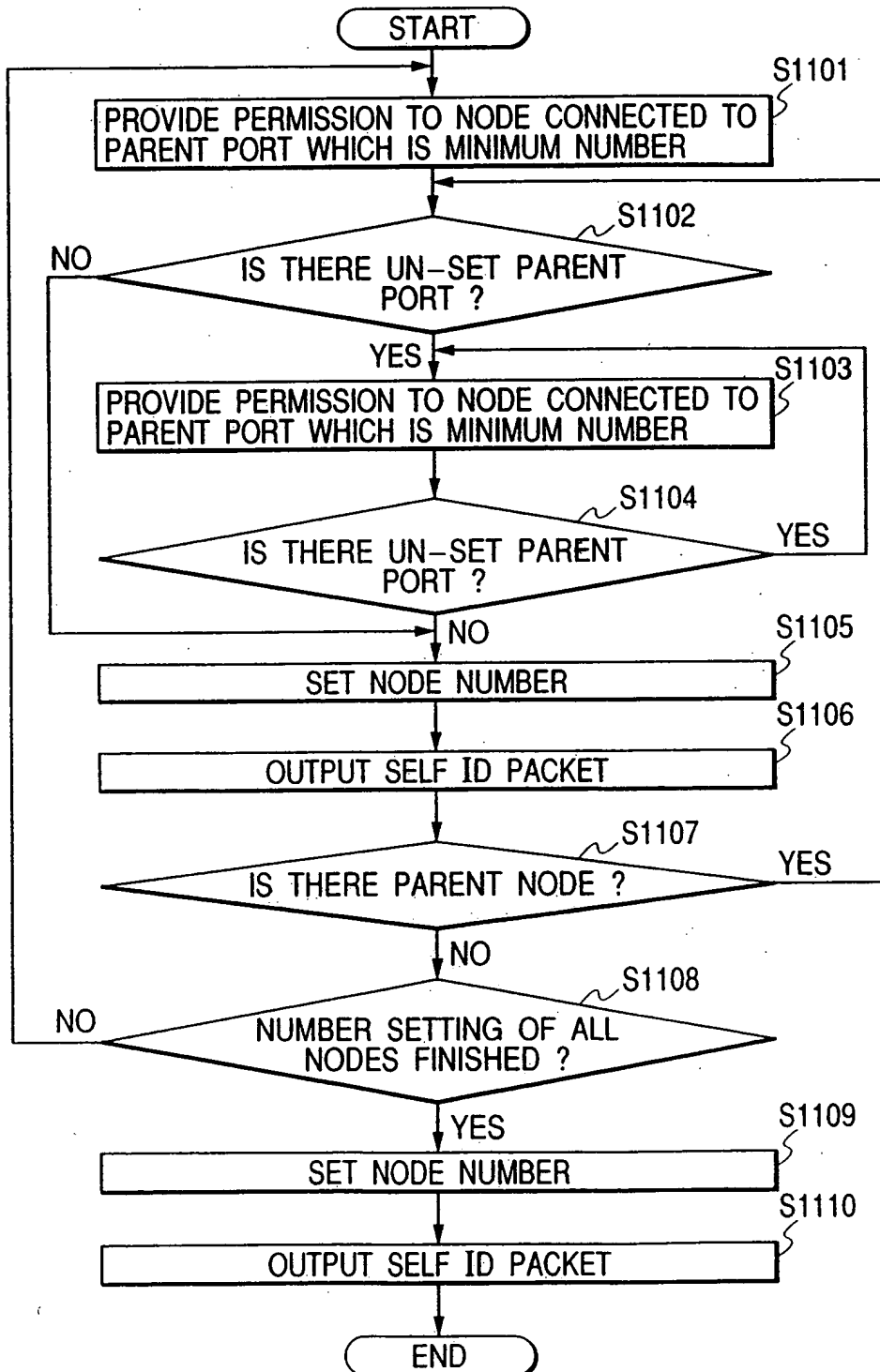


FIG. 12B

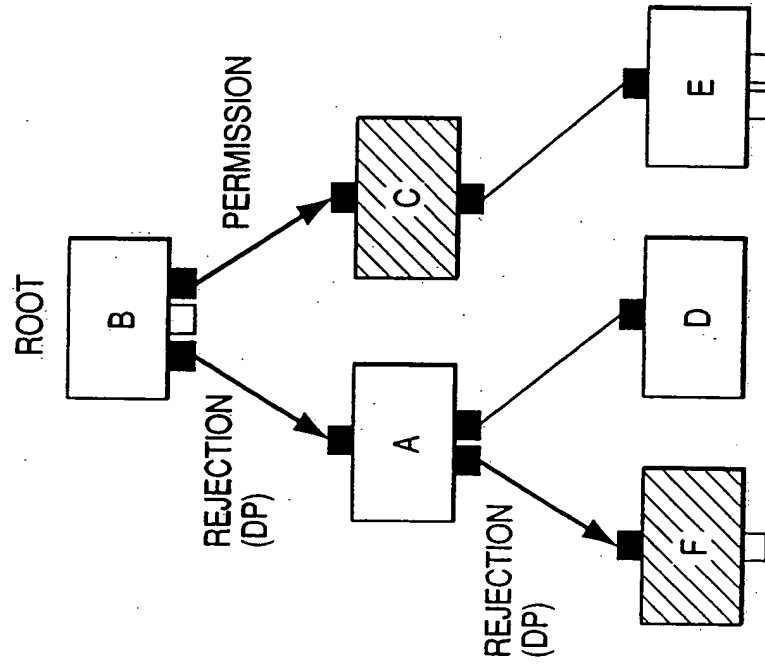


FIG. 12A

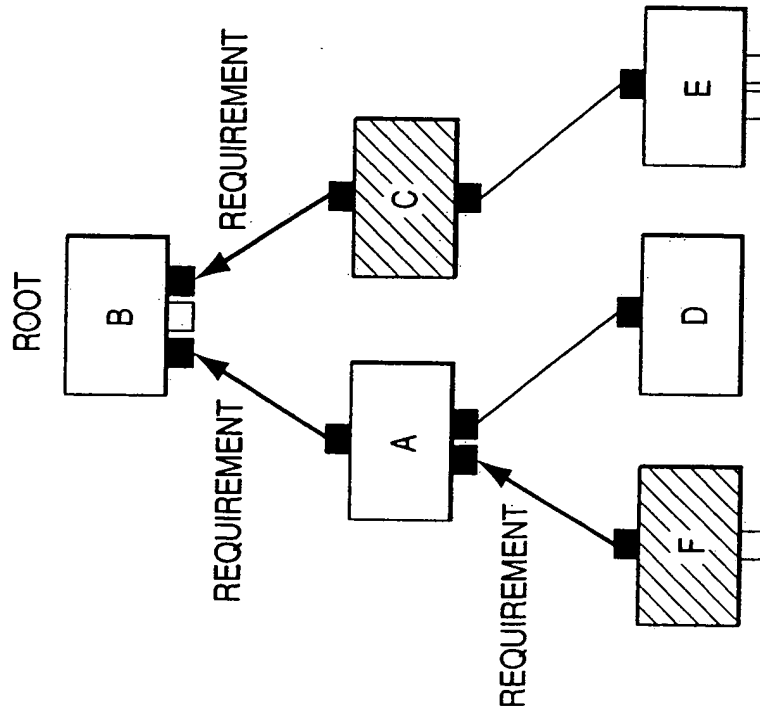


FIG. 13



FIG. 14

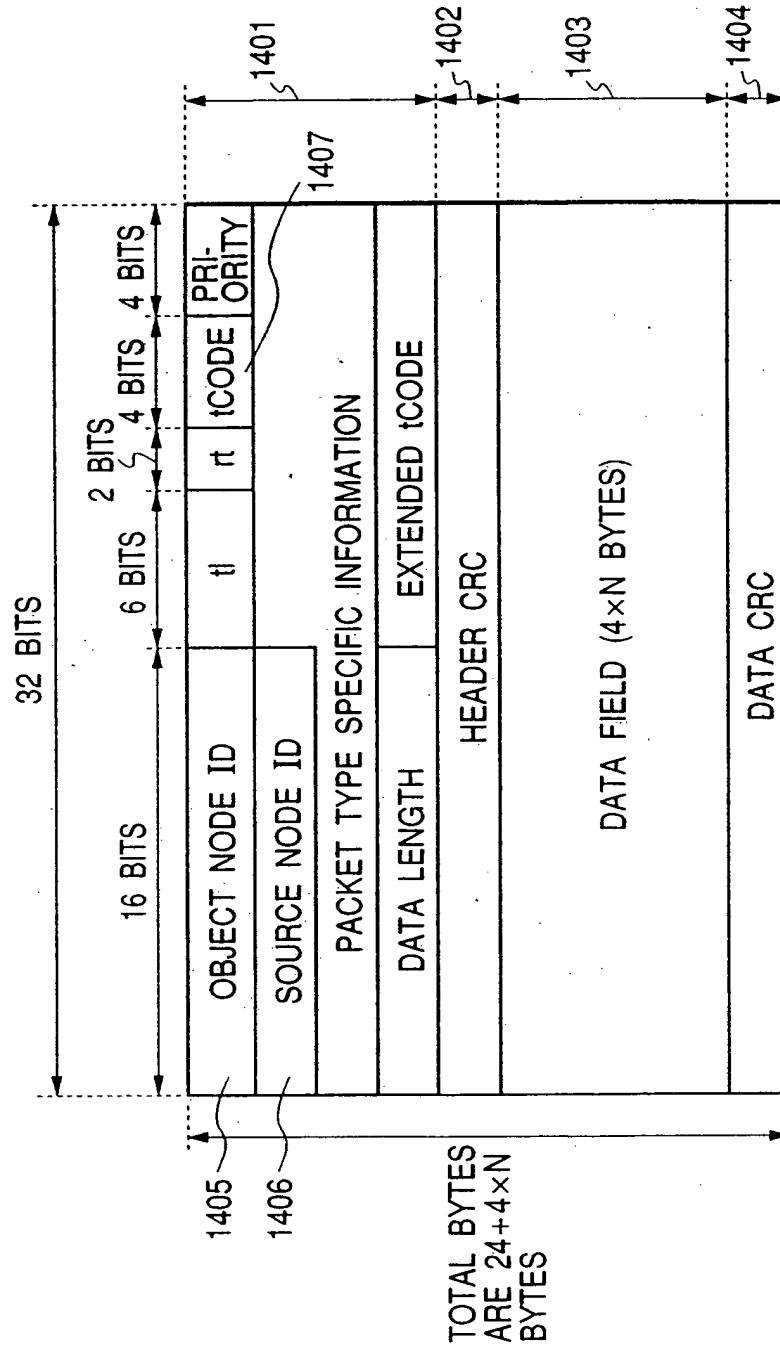




FIG. 16

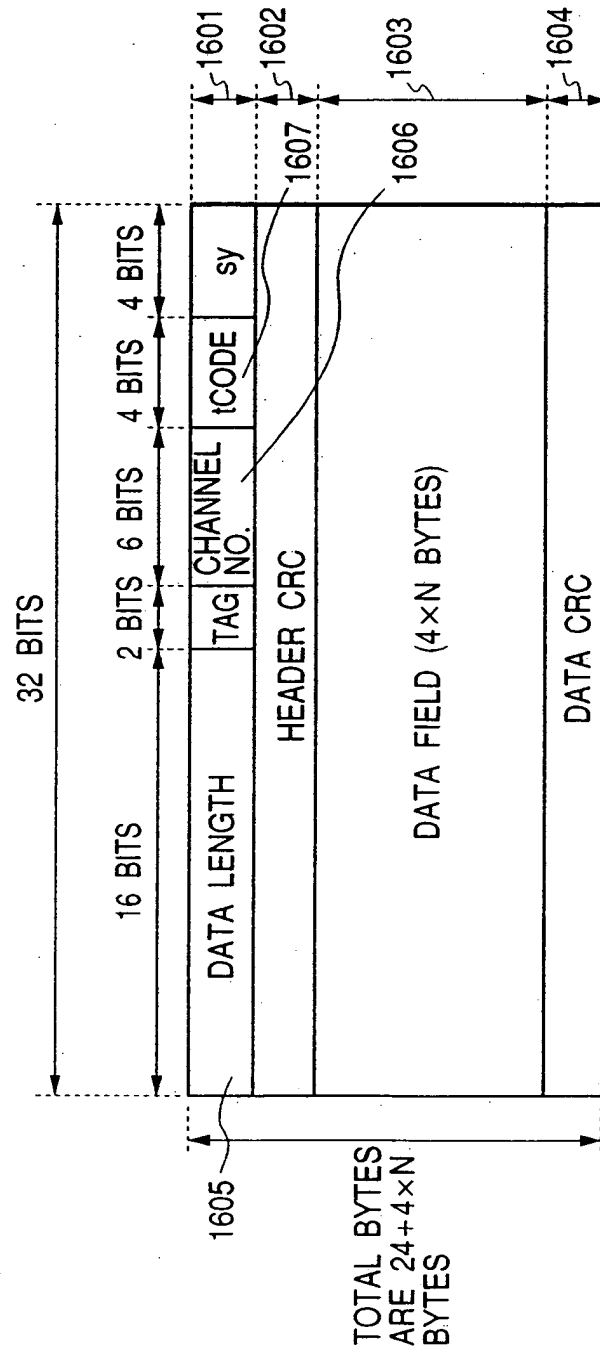


FIG. 17

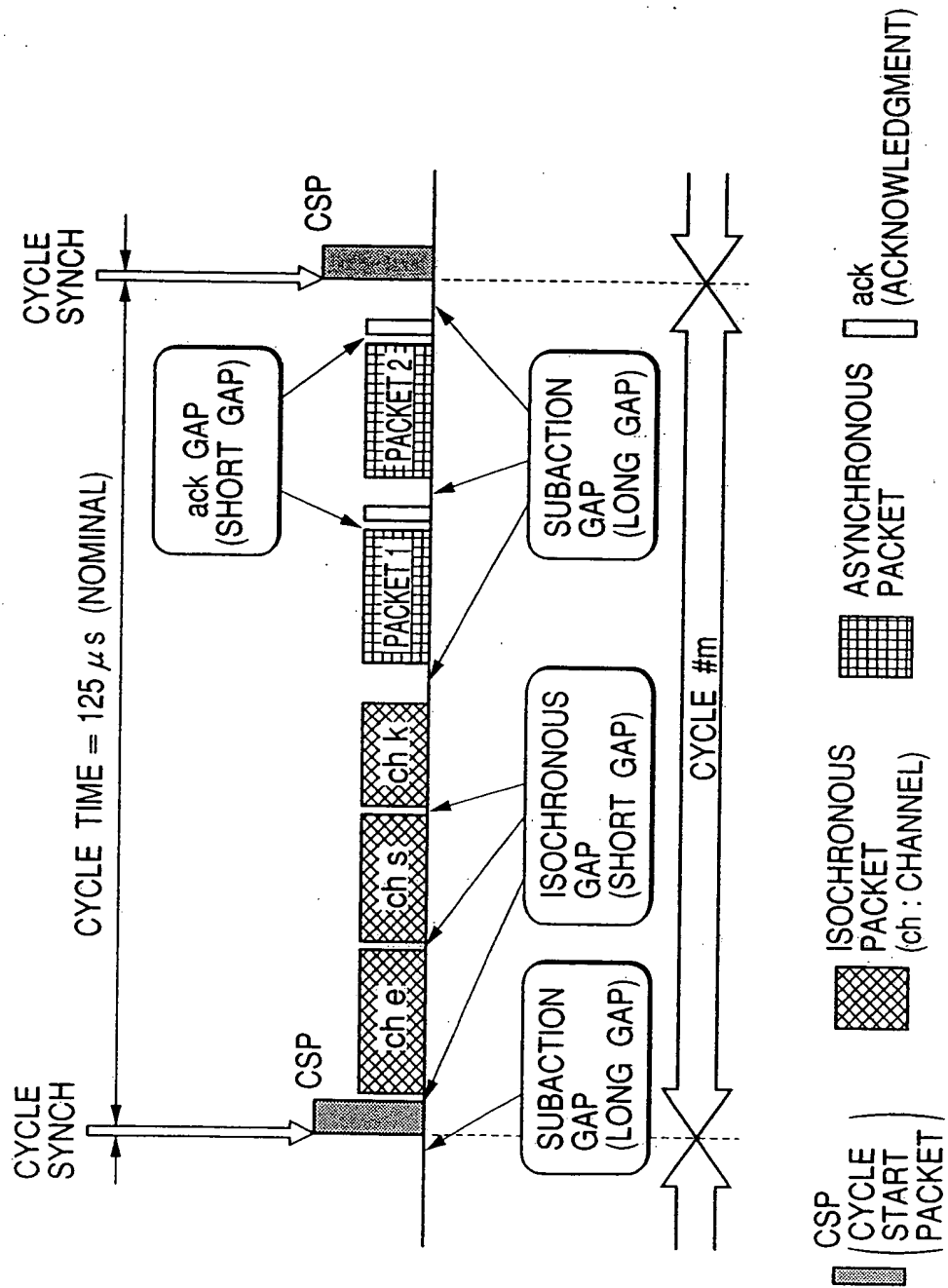


FIG. 18

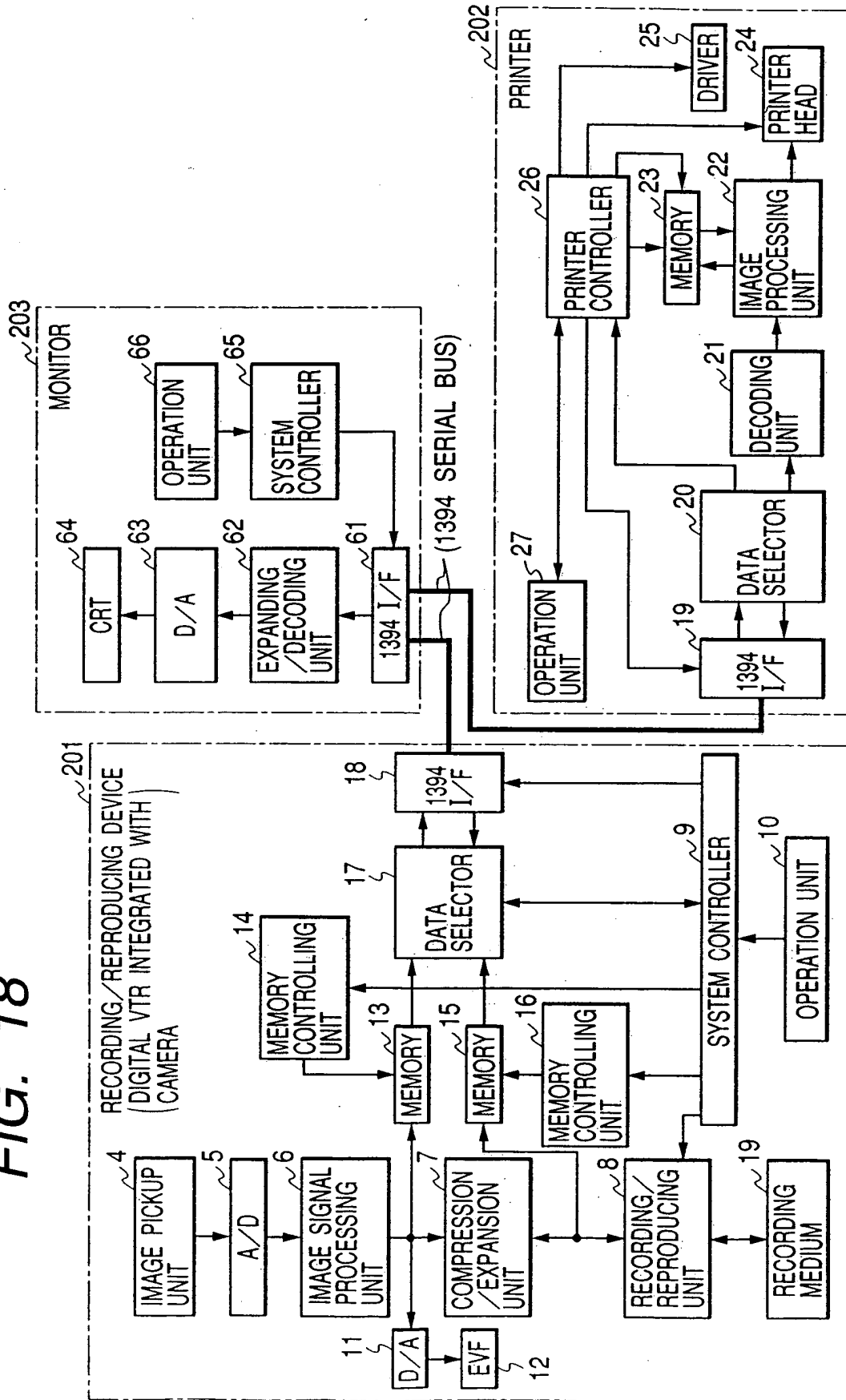


FIG. 19

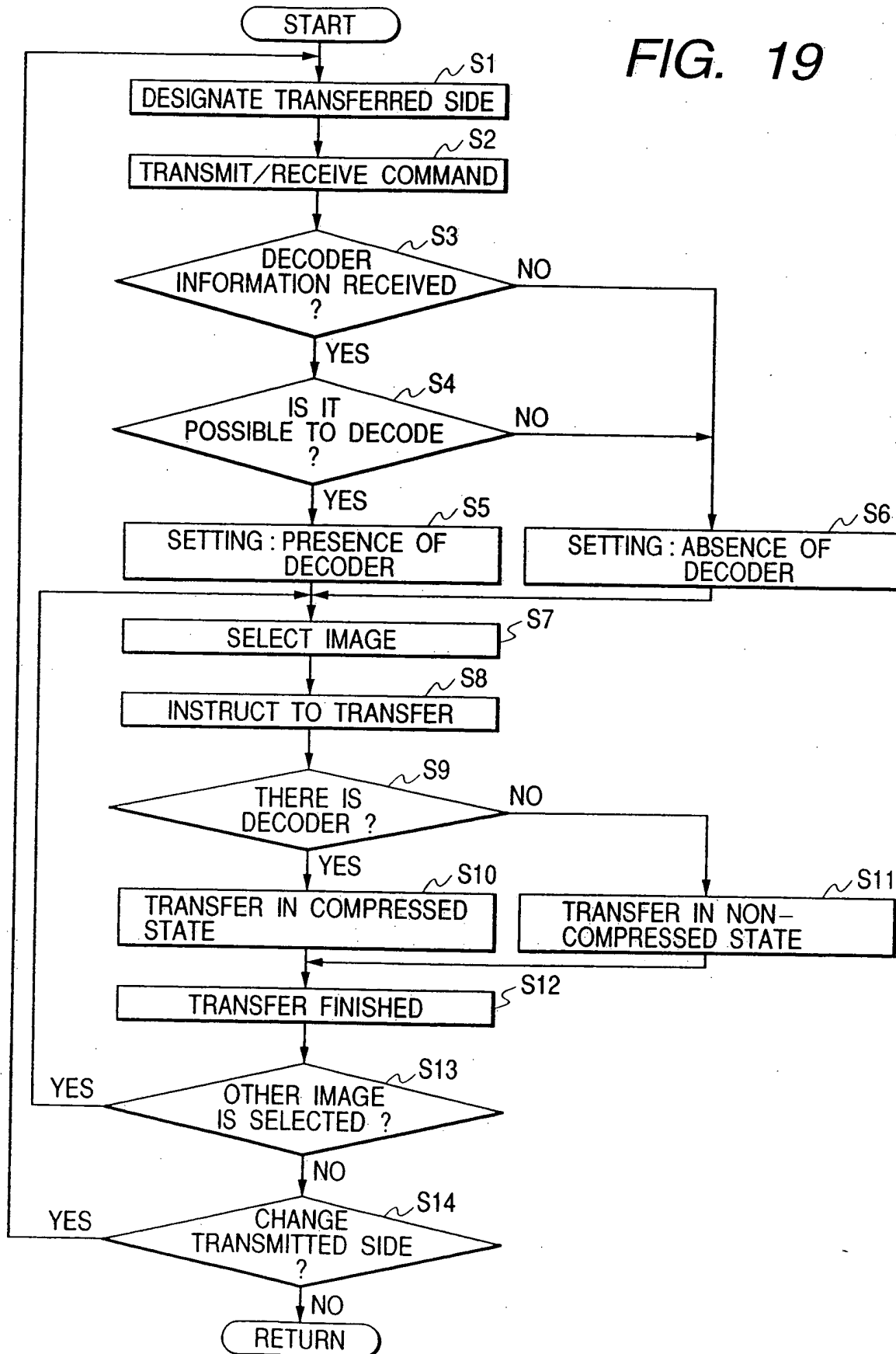


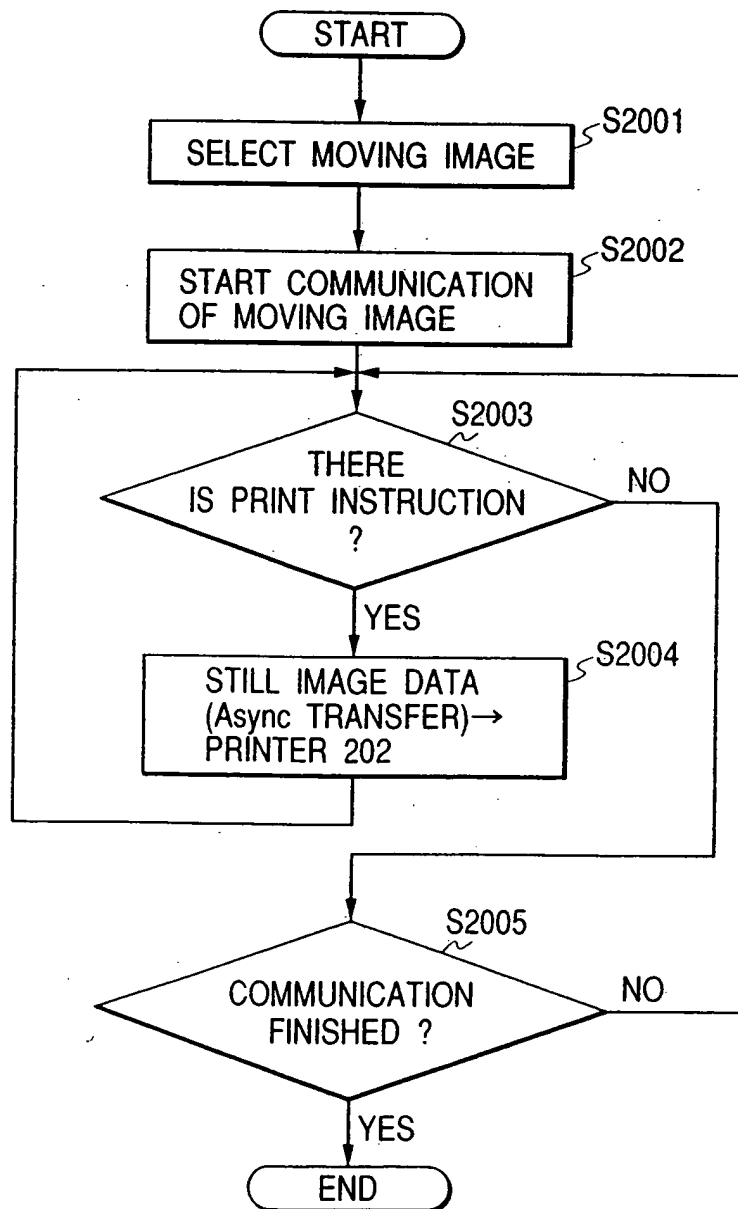
FIG. 20

FIG. 22

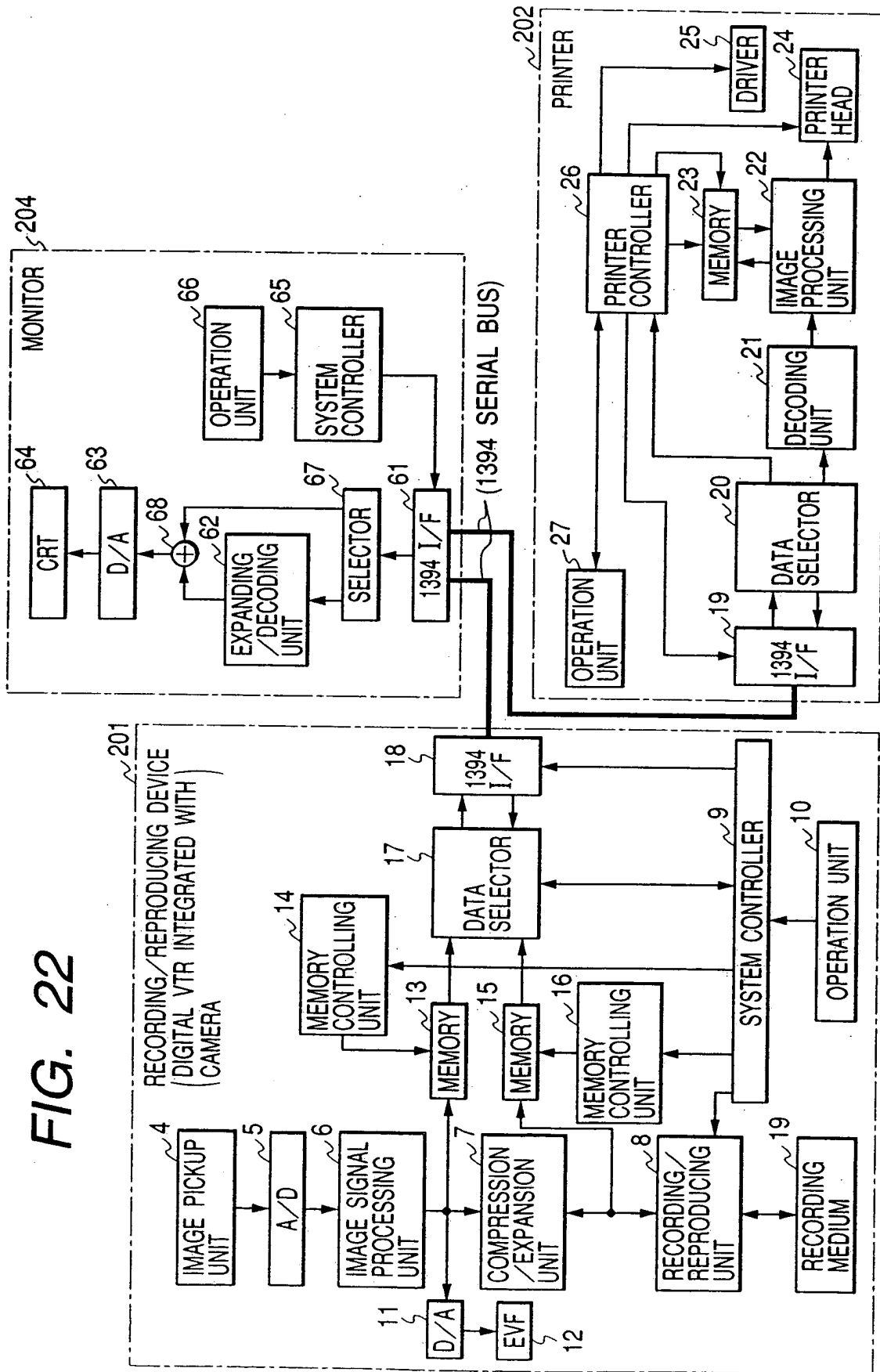


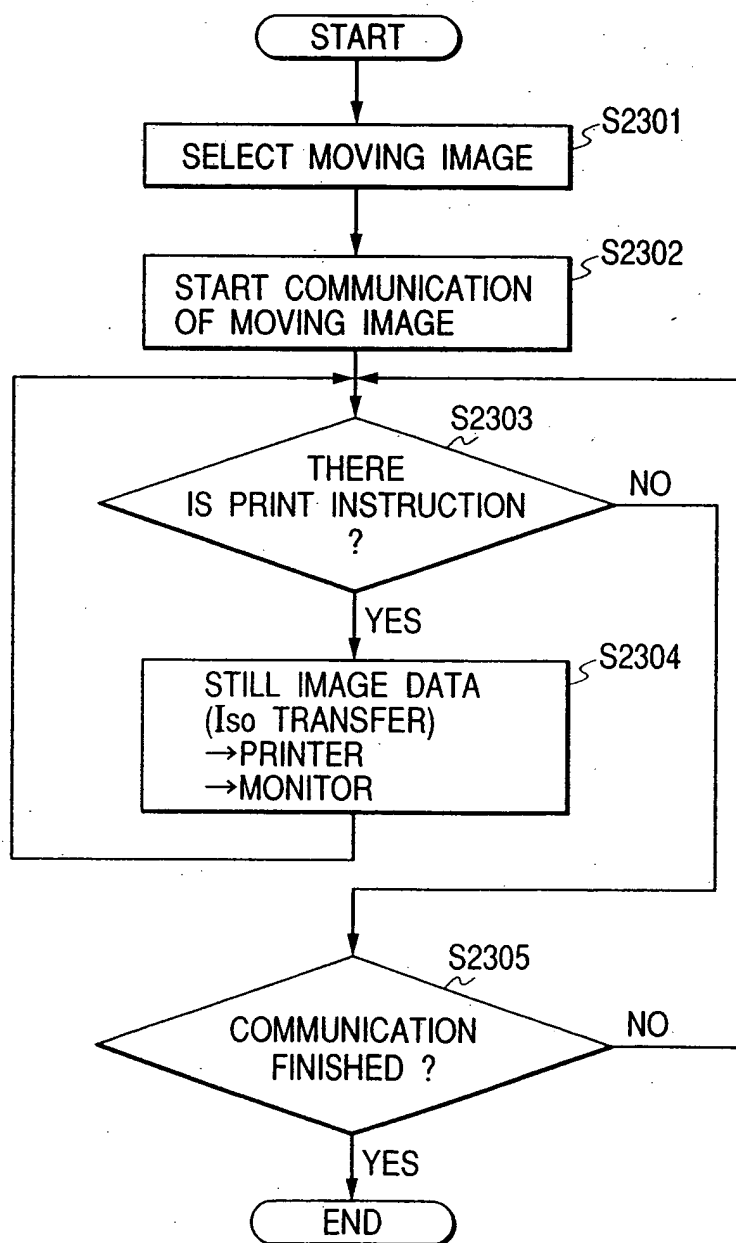
FIG. 23

FIG. 25

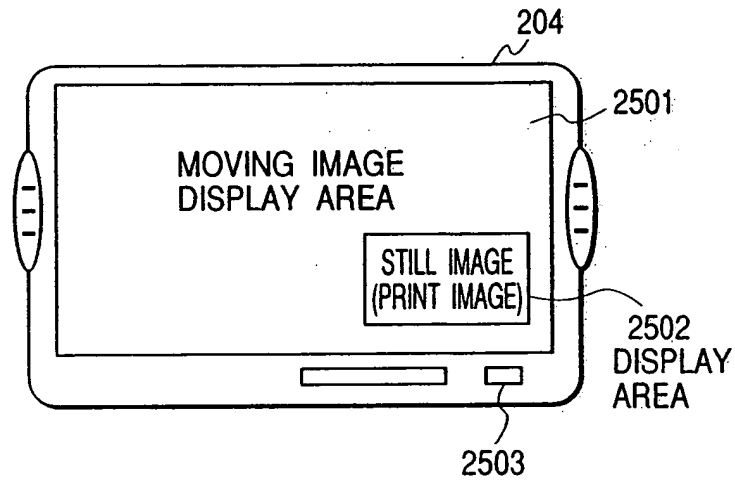
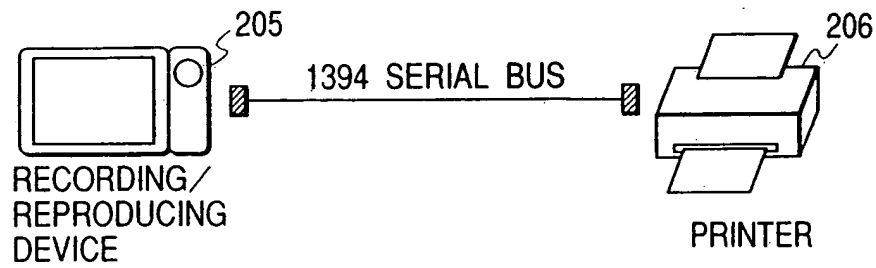


FIG. 26



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RECORDING/REPRODUCING DEVICE

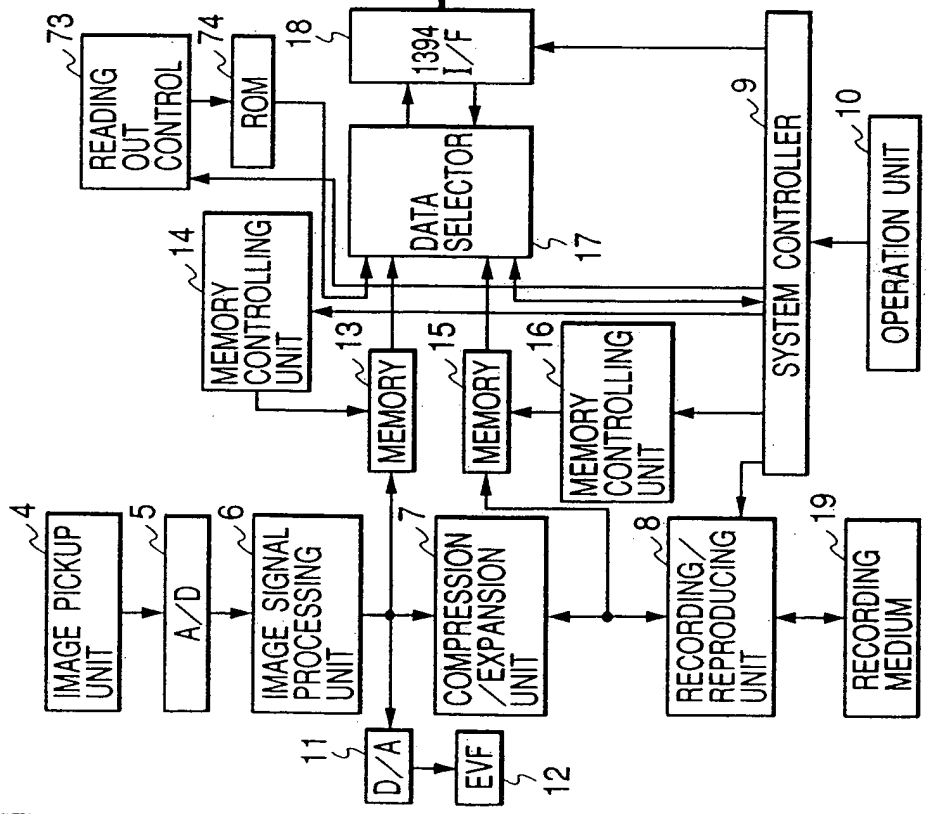
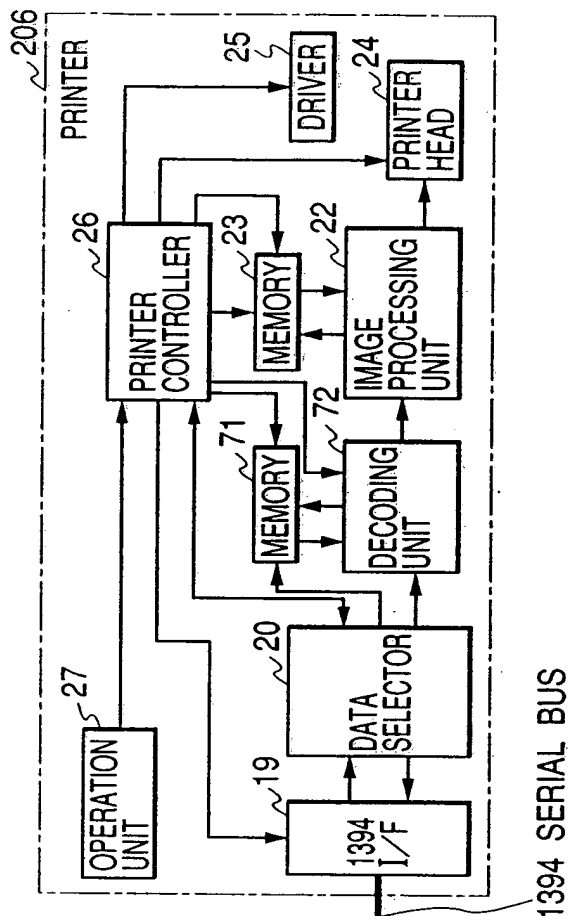


FIG. 27

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1394 SERIAL BUS

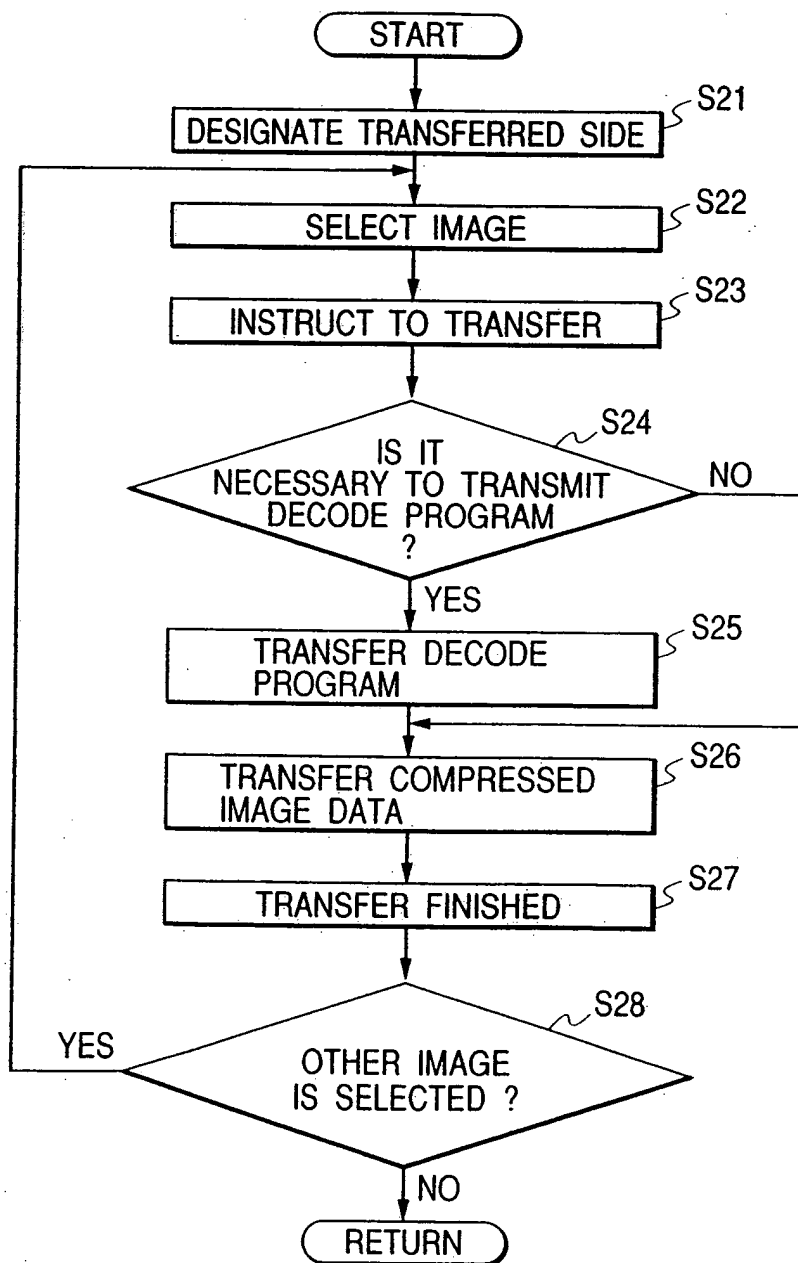
FIG. 28

FIG. 29

